# Implementing Volt/VAR Optimization with DER Penetration





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# Definitions

- CAPC = Capacitor Control
- REGC = Regulator Control
- LTCC = Load Tapchanging Transformer Control (2001D)
- OLTC = On Load Tapchanger (REG and PWR XFRM)
- FPF = Forward Power Flow
- RPF = Reverse Power Flow
- VVO= Volt/VAR Optimization
- CVR = Conservation Voltage Reduction
- $CVR_{factor} = \Delta P / \Delta V$  (0.5 typ., >1,0 is excellent)
- DA = Distribution Automation
- EOL = End of Line, as in EOL Voltage
- Reconfig = System Reconfiguration



ADVVOC = Advanced Distribution Volt/VAR Controller



## **Exploration**

#### 1547a and the New 1547

Active VAR regulation by DER

### VVO Issues:

- Line drop compensation (LDC), R and X<sub>L</sub>, or Z
- VAR-Bias vs. LDC for control of Active VAR DER
- LDC issues with reverse power flow
- Reverse power flow control modes for On-Load tapchanging Elements (OLTC = LTC Transformers and Substation Regulators)
- Inverse time vs. fixed delay for OLTC Elements



## **Exploration**

#### Substation Protection Issues:

- Radial vs. Bidirectional Fault Current Flows
  - Out-of-section (sympathy) trip concerns and mitigation
  - Remote interrupter failure protection
- Reclosing treatment :
  - Increase of 1<sup>st</sup> Shot Time Delay (from instantaneous)
  - Adaptive protection with voltage control of reclosing
- Ferroresonance on load side of feeder CBs
- Ungrounded fault backfeed into transmission protection
  - High side delta winding issue
- Summary and Q&A



## **DER Impact on VVO**



#### DER is proliferating

- Powerflows and levels change, resulting in voltage changes
- Placement of DER can change due to DA
- IEEE 1547a, and soon-to-be approved IEEE 1547-2017 (?), allow reactive as well as active powerflow output, compounding the problem



## 1547A (2014): Active Voltage/VAR Control

- Coordination and approval of the area EPS and DR operators shall be required for the DR to <u>actively</u> <u>participate to regulate the voltage</u> by <u>changes of real</u> <u>and *reactive* power</u>.
- The DR shall not cause the Area EPS service voltage at other Local EPSs to go outside the requirements of ANSI C84.1-2006, Range A.



## IEEE 1547 Addendum: IEEE 1547a

- If large amounts of DER are easily "shaken off" for transient out-of-section faults, voltage and power flow upset can occur in:
  - Feeders
  - Substations
  - Transmission
- Fault ride-through capability makes the system more stable
  - Distribution: Having large amounts of DER "shaken off" for transient events suddenly upsets loadflow and attendant voltage drops
    - Impacts include unnecessary LTC, regulator and capacitor control switching
    - If amount of DER shaken off is large enough, voltage limits may be violated
  - Transmission: Having large amounts of DER "shaken off" for transient events may upset loadflow into transmission impacting voltage, VAR flow and stability

#### This will be part of IEEE 1547-2017 (?)



## ANSI C84.1-2006

# Standard for Electric Power Systems and Equipment – Voltage Ratings

	For 120 \	V - 600 V Sys	stems	
Nominal Voltage (V)	Service Voltage (V)			
	Range A		Range B	
	Max	Min	Max	Min
120	126	114	127	110
240	252	228	254	220
480	504	456	508	440

ANSI C84.1-2006 Service Voltage Range

- Range A is the *optimal* voltage range
- Range B is *acceptable*, but not optimal





# VVO Concepts and DER Issues

What is VVO?
How do you obtain ??
CVR and what do you get out of it
How DER can cause control issues with VVO and CVR
What to do about it



## VVO

- Adjusting system voltage and pf by properly controlling OLTC and reactive assets. *Ideally*:
  - OLTC Assets used for Voltage Issues due to *Real* Power Changes
    - Load Tapchanging Transformer Controls (Substation)
    - Voltage Regulator Controls (Substation and Line)
  - Reactive Assets used for VAR regulation (loss minimization)
  - Reactive Assets used for Voltage Issues due to *Reactive* Power Changes
    - Capacitors (Line)
    - Active VAR Regulating DER (*new*)



# **VVO Controllers**

#### LTC Controls (Load Tapchanger)

- Applied on LTC Transformers in Substations
- Control voltage

#### Regulator Controls

- Applied on Regulators
   Substation and Line
- Control voltage

#### Capacitor Controls

- Applied on Pole Top Capacitor Banks
- Provide VARs and influence voltage

We'll explore some advanced applications Advanced Volt/VAR Optimization Controllers = ADVVOC











**Capacitors decrease losses proving flatter voltage profile** 





**Capacitors decrease losses proving flatter voltage profile** 





**Capacitors decrease losses proving flatter voltage profile** 



## **VVO** Results

Reduce losses

- X<sub>c</sub> counters X<sub>L</sub> of lines
- Decreased operation of OLTC elements
- Deferred capital expenditures and improved capital asset utilization
- Reduced electricity generation and environmental impacts
- Flatter voltage profile
  - Allows better CVR without low voltage violation at the end-of-line



## Forward Power and LDC



Should use high voltage block for 1<sup>st</sup> house protection!!!



## Line Drop Compensation Principle Without LDC





## Line Drop Compensation Principle With LDC





#### LDC - R, X

• Regulates voltage at a point closer to the load as voltage drops due to loss in the line because of line impedance and current





## LDC - Z

Application: Distribution bus regulation



- Concept: Increase bus voltage as the load level increases
- No individual line information
- Uses current magnitude ONLY





# Traditional Methods: Control Based Reactive Support Elements

- CAPs use "feedforward" control such as time-of-day, day, temperature, seasonality
  - Feedforward is only as good as your assumptions and correlation factors
- CAPs use voltage or VAR w/voltage override
  - Difficult to coordinate with OLTC elements using LDC with voltage or VAR w/voltage override
  - > VAR controls not much good near end of line
    - Little load flow
  - > VAR controls must be on main line
    - Voltage controls may be on line tap when "real estate" is sparse



## **CAP Voltage Control**

- Setting with Deadband
- Deadband avoids hunting





## DER Actively Controlling VAR Volt-VAr



- Why? As voltage rises, counter with absorbing VAr
- Uses droop characteristic
- Based on power and voltage sensing at PCC
- If inverter based, a "Smart" Inverter



## CAPs and DER

- As power flows and assumed reactive voltage drops can change as DER proliferates, consider changing fixed CAPs to switched to avoid overvoltage (from excessive VAR support) under high DER output conditions
- Consider active voltage (VAR) control of DER as proliferation increases





Representation in Application Sequences

- Voltage Low = Provide VARS
- Voltage High = Consume VARS







## Traditional Methods: Control Based OLTC Elements

- OLTCs use line drop compensation (LDC) to cope with line losses (R/X<sub>L</sub>, Z)
  - ➢Only as good as line model
  - May not coordinate with downline reactive elements for VAR/pf regulation
    - How can LDC control voltage sensing CAPs?
    - How can LDC control DER VAR output?



Use of VAR-Bias to Coordinate DERs/CAPs with REGs and LTCs

- VAR-Bias as a new concept to unify VVO with OLTCs and CAPs
- Use a "VAR-Bias" characteristic to change the response of the OLTC (REGC or LTCC)
- The VAR-Bias characteristic can be tailored for normal operation (non-CVR) and CVR operation
  - Normal (non-CVR) Operation: Negative VAR Bias
  - CVR Operation: Positive VAR Bias



Use of VAR-Bias to Coordinate DERs/CAPs with REGs and LTCs

- REGC and LTCC use information on VAR flow
  - Is the VAR flow out to the line (load)?
  - Is the VAR flow into the source?
- The above indicate if you are or are not at/near unity power factor
- VAR flow **into the REG or LTC** indicate the voltage downline is higher than the voltage at the REG or LTC



## Use of VAR Bias in OLTC Devices (instead of LDC)

 Use VAR-Bias control to modify behavior of the voltage adjustment with regard to *real* and *reactive* power flows to properly manipulate voltage bandcenter





## **Negative VAR-Bias**

- Called "negative" as lagging VAR causes voltage band to be lowered
- Designed to maintain unity pf and coax proper voltage asset, OLTC or reactive asset, to act depending on the cause of the voltage change
  - ✓ Voltage change from real power change, use OLTC asset
  - ✓ Voltage change by reactive power change, use VAR asset





## VAR-Bias: Near or at Unity PF





## **VAR-Bias:**

## Bandcenter Decreases with Lagging VAR Normal, Non-CVR Application





## **VAR-Bias:**

#### **Bandcenter Increases with Leading VAR Normal, Non-CVR Application Negative** Linear VAR Bias 124V As voltage rises: **CAPs switch OFF DER absorbs VAr** 122V Voltage lowers from decrease in VAr 120V **NO Tap** Command 118V 116V Lagging VARs (+) Leading VARs (-)



#### Normal, Non-CVR Application Negative Linear VAR Bias



## Normal Operation: Negative VAR-Bias

#### **NORMAL OPERATION (non-CVR)**


(+) Lagging VARs

124V

123V 122V

121

119V 118V 117V 116V

120V





Leading VARs

(-)



### **NORMAL OPERATION (non-CVR)**



DER outputs VAr
Voltage and VAr

normalize

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### Normal, Non-CVR Application Negative Linear VAR Bias



# Normal Operation: Negative VAR-Bias

### NORMAL OPERATION (non-CVR)

- Inductive load decreases, pf leads, voltage rises.
- REG bandcenter rises.
- CAPs switch off, DER consumes VAr
- Voltage and VAr normalize





### Normal, Non-CVR Application Negative Linear VAR Bias



# Normal Operation: Negative VAR-Bias

### NORMAL OPERATION (non-CVR)

- Resistive load decreases, pf remains the same, voltage rises
- REG taps down, voltage normalizes
- CAPs and DER do not change VAr output





### Normal, Non-CVR Application Negative Linear VAR Bias



# Normal Operation: Negative VAR-Bias

### **NORMAL OPERATION (non-CVR)**



Resistive load increases, pf leads, voltage decreases

- REG taps up, voltage normalizes
- CAPs and DER do not change VAr output



### Voltage Bandcenter and Bandwidth: LTC/REG, CAP, DER





- CAPS and DER furthest away from source have shorter time delay than upline similar devices
- This examples uses CAPs before DER
  - Assuming DER charges for reactive support



### Voltage Settings and Timings: LTC/REG, CAP, DER



- CAPS and DER furthest away from source have shorter time delay than upline similar devices
- This examples uses CAPs switching before DER, assuming DER charges for reactive support
- REGs use VAR-Bias with larger bandwidth and longer time delays than CAPs or DER



# VAR-Bias and Deep CVR ■ How low can you go? ✓ Lower than you may think!







# VVO and CVR - Why

- Lowering distribution voltage levels during <u>peak</u> periods to achieve <u>peak</u> demand reductions
- Reducing voltage levels for <u>longer periods</u> to achieve electricity <u>conservation</u>
- Reducing energy losses in the electric distribution system

Benefits include deferral of capital expenditures, energy savings, and greater operational flexibility and efficiency

Voltage and Reactive Power Management – Initial Results: US DOE, 12/12



## **Conservation Voltage Reduction**

### Goal of voltage reduction is to reduce load

•V= I \* R for constant Z load

•The lower the V the lower the I

- •The lower the I, the lower the  $I^2R = W$  (constant Z load)
  - Ex., incandescent lights, strip heaters
- •Not true if load is not constant power type (constant PQ load):
  - Ex., motors, power supplies

### Can be deployed at:

- All times
- For load reduction periods (peak reduction)
- During system emergencies when the voltage is collapsing due reactive load exceeding available supply



# Load Models and CVR Factor

- Load models
  - Constant Power (PQ)
  - Constant Impedance (Z)
  - Constant Current (I) —

Load current changes inversely to the change in voltage

Load current changes linearly with the change in delivered voltage, and the demand varies as a squared function of the voltage change (ex., incandescent bulb)

Power delivered to the load varies linearly with the change in voltage delivered to the load

Constant Power (PQ or kVA)	Constant Impedance (Z)	Constant Current (I)
Motors (at rated load)	Incandescent/Dimmable LEDLighting	Welding Units
Power Supplies	Resistive (Strip) Water Heaters	Electroplating
Fluorescent/LED Lighting	Electric Stoves	
Washing Machines	Clothes Dryers	

 $CVR_f = \Delta P / \Delta V$ 

- 0.5 to 0.7 is typical
- Greater than 1 is really good

**Evaluating Conservation Voltage Reduction with WindMil® - Milsoft** 



# Load Models and CVR Factor



 $CVR_f = \Delta P / \Delta V$ 

- 0.5 is typical
- Greater than 1 is really good

0.9 0.92 0.94 0.96 0.98 1.0 1.02 1.04 1.06 1.08 1.1

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Constant Power (PQ or kVA)	Constant Impedance (Z)	Constant Current (I)
Motors (at rated load)	Incandescent/Dimmable LEDLighting	Welding Units
Power Supplies	Resistive (Strip) Water Heaters	Electroplating
Fluorescent/LED Lighting	Electric Stoves	
Washing Machines	Clothes Dryers	

### **Evaluating Conservation Voltage Reduction with WindMil® - Milsoft**



# **Negative VAR-Bias**

- Called "negative" as lagging VAR causes voltage band to be lowered
- Designed to maintain unity pf and coax proper voltage asset, OLTC or reactive asset, to act depending on the cause of the voltage change
  - ✓ Voltage change from real power change, use OLTC asset
  - ✓ Voltage change by reactive power change, use VAR asset





# **Positive VAR-Bias**

- Called "positive" as leading VAR causes voltage band to be lowered
- Designed to cause leading pf and raise voltage at end of the feeder
  - ✓ Allows head of feeder to lower voltage near ANSI C84.1 low limits
  - $\checkmark\,$  Fosters greater power reduction during CVR operation





### **CVR Application**

Positive Linear VAR Bias



# CVR Operation: Positive VAR-Bias

### **CVR OPERATION**



- REG forces voltage lower
- CAPs begin to switch on and DER outputs VAr







# CVR: REGs/LTC with DERs/CAPs



- For CVR, forcing overVAr on feeder causes end of line voltage to rise
- You can have a deeper voltage reduction at the beginning of the line where most of the load is located (EPRI Green Circuits)



# VAR-Bias Take Away

The cost is ADVVOCs, which you need anyway

- The the the the the the the the
- > No extensive comms system required
- >NO DMS required

Feedback loop from CAPs to OLTCs to modify voltage control is made from VAR flow/direction



# Use of Powerflow Direction Change by REGC/LTCC

### **Terminology Cross Reference**

Beckwith Reverse Power Mode	Cooper/Siemens Reverse Power Mode
Block	Locked Forward Mode
Regulate Forward (Ignore) *	Reverse Idle Mode
Regulate Reverse	Bi- Directional Mode
Return to Neutral *	Neutral Idle Mode
Regulate in Reverse (Measured) *	Bi-Directional Mode
Distributed Generation	Cogeneration Mode
Auto Determination	None
*Low Current block feature must al	so be enabled to be equivalent to this
Cooper Reverse Power Mode.	



### **REGC/LTCC:** Reverse Power Method Discussion

oints   ( New File)			?
Profile 1 Profile 2 Profile 3	Profile 4		
General Line Drop Compensation © R, X © Z Time Delay Selection © Definite Time © Inverse Time Paris Times Time © Instant Reset	Forward Power Band Center Band Width Definite Time	120.0         100.0         ▶         135.0 (V)           2.0         1.0         ▶         100.0 (V)           30         1         ▶         360 (sec)	
Power Direction Bias None	LDC-Z LDC Resistance	0 0 ∢ → 72 (V) 0 -72 ∢ → 72 (V)	
Step 1       2.5       0.0       10.0 (%)         Step 2       5.0       0.0       10.0 (%)         Step 3       7.5       0.0       10.0 (%)         Standard VR       © Disable       © Enable         Smart VR       © Disable       © Enable         Com VR Turnoff Timer       0       4       999 (Min)         Save VR at Power Off       © Don't Save       © Save         Limit and Runback       128.0       95.0       135.0 (V)         Runback Deadband       20       1.0       4.0 (V)         Block Lower       114.0       95.0       135.0 (V)         Runup Deadband       20       1.0       4.0 (V)         Runup © Disable       © Enable       Current Limit	Reverse Power Operation Band Center Band Width Definite Time LDC-Z LDC Resistance LDC Reactance	Block  Sock  Regulate Forward (ignore) Regulate Forward (ignore) Regulate Revense Return to Neutral Regulate Revense(Measured) Distributed Generation Auto Determination Auto Determination V 0 -72  V 0 -72  V 0 72  V 0 7 7  V 0 7  V 0	
VAr Bias VAr Bias Method :      O Disable      Step	C Linear		1
UndorRefresh		Save Close	

### **RPF** Selection



### **Return to Neutral**

- Return to Neutral drives tap position to neutral and then stops
  - Use where small unpredictable change in voltage may be encountered on RPF side of REG
  - "Feel safe" strategy when one cannot distinguish the source strength of the RFP
    - Is it DER, and possible weak?
    - Is it DA, and strong?
  - Can be risky as there is no control once at the neutral tap

-Forward Power			
Band Center	120.0	100.0 🔳	▶ 135.0 (V)
Band Width	2.0	1.0 🔳	▶ 10.0 (V)
Definite Time	30	1 🔳	▶ 360 (sec)
LDC-Z	0	0 🔳	▶ 72 (V)
LDC Resistance	0	-72 🔳	▶ 72 (V)
LDC Reactance	0	-72 🔳	▶ 72 (V)
Reverse Power			
Operation	Return to Neu	utral	▼
Operation	Return to Neu Reverse P	utral ower Vendor Cros	▼ s Reference
Operation Development	Return to Neu Reverse P 120.0	utral ower Vendor Cros 100.0	s Reference
Operation Band Center Band Width	Return to Neu Reverse P 120.0 2.0	utral ower Vendor Cros 100.0	▼ s Reference ▶ 135.0 (V) ▶ 10.0 (V)
Operation Band Center Band Width Definite Time	Return to Neu Reverse P 120.0 2.0 30	utral ower Vendor Cros 100.0 4 1.0 4 1 4	▼ s Reference ↓ 135.0 (V) ↓ 10.0 (V) ↓ 360 (sec)
Operation Band Center Band Width Definite Time LDC-Z	Return to Neu Reverse P 120.0 2.0 30 0	utral ower Vendor Cros 100.0 ◀ 1.0 ◀ 1 ◀ 0 ◀	★
Operation Band Center Band Width Definite Time LDC-Z LDC Resistance	Return to Net Reverse P 120.0 2.0 30 0 0	utral ower Vendor Cros 100.0 4 1.0 4 1 4 0 4 -72 4	▼ s Reference 135.0 (V) ► 10.0 (V) ► 360 (sec) ► 72 (V) ► 72 (V)





### Block

- Block inhibits automatic operation, leaving regulator on present tap
  - Use where source of RPF is not expected to change voltage on RPF side of REG
  - Also a "feel safe" strategy when one cannot distinguish the source strength of the RFP
    - Is it DER, and possible weak?
    - Is it DA, and strong?
  - Can be risky as there is no control and the voltage begins to deviate

- Forward Power			
Band Center	120.0	100.0 •	▶ 135.0 (V)
Band Width	2.0	1.0 •	▶ 10.0 (V)
Definite Time	30	1	360 (sec)
LDC-Z	0	0 🔳	▶ 72 (V)
LDC Resistance	0	-72 🔳	📄 🕨 72 (V)
LDC Reactance	0	-72 🔳	▶ 72 (V)
D			
Reverse Power			
Reverse Power	Block		<b>•</b>
Reverse Power	Block Reverse P	ower Vendor Cros	▼ s Reference
Reverse Power	Block Reverse P 120.0	ower Vendor Cros	s Reference
Reverse Power Operation Band Center Band Width	Block Reverse P 120.0 2.0	ower Vendor Cros	▼ s Reference ▶ 135.0 (V) ▶ 10.0 (V)
Reverse Power Operation Band Center Band Width Definite Time	Block Reverse P 120.0 2.0 30	ower Vendor Cros	▼ s Reference 135.0 (V) 10.0 (V) 360 (sec)
Reverse Power	Block Reverse P 120.0 2.0 30 0	ower Vendor Cros	▼ s Reference 135.0 (V) ▶ 135.0 (V) ▶ 10.0 (V) ▶ 360 (sec) ▶ 72 (V)
Reverse Power Operation Band Center Band Width Definite Time LDC-Z LDC Resistance	Block Reverse P 120.0 2.0 30 0 0	ower Vendor Cros	▼ s Reference 135.0 (V) 10.0 (V) 360 (sec) 72 (V) 72 (V)





### Ignore: Regulate Forward

- Regulate Forward (Ignore) continues control action as though forward power flow continued to exist.
  - Uses same settings with normal forward power flow
  - May use with small amounts of RPF, or when you need to drive down voltage due to DER causing a voltage rise
  - With strong reverse power flows, LDC will drive voltage down

-Forward Power	
Band Center 110	0.0 100.0 ◀ _ ▶ 135.0 (V)
Band Width 4.	0 1.0 ◀ ▶ 10.0 (V)
Definite Time	1 • 360 (sec)
LDC-Z	) 0 ◀ ▶ 72 (V)
LDC Resistance	-72 📢 📄 72 (V)
LDC Reactance	-72 📢 💽 72 (V)







- Regulating forward, +LDC raises bandcenter as FPF becomes larger
- Regulating **forward**, -LCD *lowers* bandcenter as **RPF** becomes larger



### **Regulate Forward and LDC** 15 -15V 10 **10V** 5 **5**V Forward Power Reverse Power Voltage **2V** 0 -5 -10 -100 0 50 -50 100

Notice that if the current is reverse, LDC drops the voltage instead of raising it

Percent of Full Load Current

### DG Mode: Regulate Forward with New LDC Settings

- Regulate Forward (DG Mode)
  - This mode of operation is the same as the Ignore mode, plus provides ability to change line drop compensation (LDC)
  - Use where DER power output is large enough to warrant new LDC settings



- A separate set of LDC settings can be specified which will be applied during reverse power
  - This can include LDC factor magnitudes, signs and the use of R and  $X_{\rm L}$  , or Z
  - VAR-Bias may also be selected





- [Volts subtracted from Bandcenter]
- Regulating **forward**, -LDC *raises* bandcenter as **RPF** becomes larger
- Regulating forward, +LCD lowers bandcenter as RPF becomes larger

# REGC/LTCC: Reverse Power, "Regulate Reverse"

- Regulate Reverse (Calculated):
  - Voltage Sensing: With RPF, control uses tap position knowledge and FPF side voltage
  - Regulates according to reverse power settings
    - Use where RPF source to OLTC is a stronger source
    - Regulate voltage on the RPF side of the OLTC
      - Typically used for reconfiguration
- Regulate Reverse (Measured):
  - Voltage Sensing: With RFP, control switches its voltage sensing input to a RPF side VT
     – RFP side VT must be available
  - Regulates according to reverse power settings
    - Use where RPF source to REG is a stronger source
    - Regulate voltage on the RPF side of the REG
      - Typically use for reconfiguration









### **REGC/LTCC:** Reverse Power, "Regulate Reverse"

-Forward Power		
Band Center	120.0	100.0 🔳 📄 135.0 (V)
Band Width	2.0	1.0 📕 🕨 10.0 (V)
Definite Time	30	1 📕 🕨 360 (sec)
LDC-Z	0	0 💽 🕨 72 (V)
LDC Resistance	0	-72 💶 🕨 72 (V)
LDC Reactance	0	-72 🔳 📄 72 (V)
Reverse Power		
l l		
Operation	Regulate Rev	/erse
Operation	Regulate Rev Reverse P	ower Vendor Cross Reference
Operation   Band Center	Regulate Rev Reverse P	verse  wer Vendor Cross Reference 100.0
Operation   Band Center Band Width	Regulate Rev Reverse P 120.0 2.0	verse ower Vendor Cross Reference 100.0 ▲ ▶ 135.0 (V) 1.0 ▲ ▶ 10.0 (V)
Operation   Band Center Band Width Definite Time	Regulate Rev Reverse P 120.0 2.0 30	verse       ▼         ower Vendor Cross Reference       100.0       ▲       ▶       135.0 (V)         1.0       ▲       ▶       10.0 (V)         1       ▲       ▶       360 (sec)
Operation   Band Center Band Width Definite Time LDC-Z	Regulate Reverse P 120.0 2.0 30 0	verse       ✓         ower Vendor Cross Reference       135.0 (V)         100.0       ▲       ▶       135.0 (V)         1.0       ▲       ▶       10.0 (V)         1       ▲       ▶       360 (sec)         0       ▲       ▶       72 (V)
Operation   Band Center Band Width Definite Time LDC-Z LDC Resistance	Regulate Rev Reverse P 120.0 2.0 30 0 0	verse       ▼         ower Vendor Cross Reference       135.0 (V)         100.0       ▲       ▶       135.0 (V)         1.0       ▲       ▶       10.0 (V)         1       ▲       ▶       360 (sec)         0       ▲       ▶       72 (V)         -72       ▲       ▶       72 (V)

- "Regulate Reverse"
  - Calculated
- Regulates reverse with new voltage settings and LDC values
- Use with strong RPF source (reconfig)
- Uses tap position and calculates voltage on previous source side of regulator
- Additional VT not needed



### **REGC/LTCC:** Reverse Power, "Regulate Reverse"

Forward Power		
Band Center	120.0	100.0 🔳 💽 135.0 (V)
Band Width	2.0	1.0 📕 🕨 10.0 (V)
Definite Time	30	1 📕 🕨 360 (sec)
LDC-Z	0	0 🔳 🕨 72 (V)
LDC Resistance	0	-72 🛃 📄 72 (V)
LDC Reactance	0	-72 🚺 📄 72 (V)
Reverse Power —		
Operation	Regulate Rev	verse(Measured)
Operation	Regulate Rev Reverse P	verse(Measured)
Operation	Regulate Rev Reverse P 120.0	verse(Measured) ower Vendor Cross Reference 100.0 ▲ ▶ 135.0 (V)
Operation	Regulate Rev Reverse P 120.0 2.0	verse(Measured)
Operation Band Center Band Width Definite Time	Regulate Reverse P	verse(Measured)
Operation Band Center Band Width Definite Time LDC-Z	Regulate Reverse P	verse(Measured)       ▼         tower Vendor Cross Reference       100.0       ▲       ▶       135.0 (V)         1.0       ▲       ▶       10.0 (V)         1.0       ▲       ▶       10.0 (V)         1       ▲       ▶       360 (sec)         0       ↓       ▶       72 (V)
Operation Band Center Band Width Definite Time LDC-Z LDC Resistance	Reverse P 120.0 2.0 30 0 0	Verse (Measured)       ▼         tower Vendor Cross Reference       100.0       ▲       ▶       135.0 (V)         1.0       ▲       ▶       10.0 (V)         1       ▲       ▶       360 (sec)         0       ▲       ▶       72 (V)         -72       ▲       ▶       72 (V)

- "Regulate Reverse"
  Measured
- Regulates reverse with new voltage setpoints and LDC values
- Use with strong RPF source (reconfig)
- Uses additional VT on previous supply side of regulator



# Issues with DA and DER

- ➢ Reverse Power Flow (RPF)
- Both a reconfig and DER may cause RPF
  - With DER (weaker source than system), forward regulation should be employed
  - With *reconfig* (strong source switches), *reverse* regulation should be employed

How do we know weak and strong source if you have mix of DA and DER?



High Penetration of DER and/or DA on Distribution Systems Requires Smart Technology to obtain VVO/CVR



- How do you know after a reconfiguration which side of a regulator has the string source?
- How do you control caps relocated due to reconfiguration
- Normal power from Utility to load
  - Utility strong source
- DER may backfeed
  - Typically a weaker source
- What to do with power reversal from sectionalizing?
- What to do with power reversal from DER?
- What to do about LDC with DER influencing?

# **Sample DA Scenarios**



What does DA do to power flow and source strength on line sections?



### Volt/VAR Control Considerations from DA



- Normal open loop
- Uses recloses to perform FLISR
- V/VAR feeder devices employed: REGC and CAPC





### Volt/VAR Control Considerations from DA



• Fault occurs on feeder





### Volt/VAR Control Considerations from DA



- Fault is cleared by 52 (O/C trip and LO) and 79 (27)
- Tie 79 closes (uses H/D logic)
- Power is restored to most of loop system
- Reverse power flow occurs on some section of the newlyconfigured feeder



### Voltage Control Considerations from DA: REGC



How to address RFP:

- 1. Do nothing (does not work; REG LDC causes operational errors)
- 2. Use communications to control by setpoint or setting group
- 3. Use change of **powerflow direction** to change to a new **predetermined** control mode
- 4. Use change of **powerflow direction and source strength** (by REGC measurement) to initiate *autodetermination* of **best** control mode


## RPF: Why We Care????

- With high penetration levels of DA and/or DER on the distribution system it is becoming more common to have the voltage regulators deal with reverse power situations
- The solution to the OLTC problem gets complicated as the control needs to know (or assume) the source of reverse power.
- It is important to select the correct reverse power mode of operation for voltage regulators otherwise dangerous high or low voltage levels may result causing equipment damage or misoperations





Notice that if the current is reverse, LDC drops the voltage instead of raising it

Percent of Full Load Current



# The Reverse Power Flow (RPF) Problem

- It's all about source strength
  - If the source is weak, small impact (most DER)
  - If the source is strong, big impact (reconfiguration)

- Impacts of strong source RPF:
- e RPF:
  - Drives LDC the wrong way
  - Regulation should be in the now reverse direction
    - The tail *cannot* wag the dog





# **No RPF Source**





# Weak RPF Source





# No RPF Source: Open Loop





# Strong FPF Source: Reconfig





# Strong RPF Source: Reconfig





# How Can One Know About Source Strength



- Guess it, assume it
- Cheap and easy if one can make assumptions or guess
- LTC or REG makes RPF determination and goes into <u>predetermined</u> response mode, either:
  - No DER on line, and the only way you can have RFP is a reconfiguration with a new source direction (assume new strong source)
  - No reconfiguration possible, so only DER can cause RPF



# Knowing Relative Source Strength is KEY





- Use "Autodetermination"
  - Reverse Power Flow Source Strength
    Determination
    - Control determines relative source strength
  - Why it is important
    - <u>Weak</u> source (DER) results in continuing **forward** regulation
      - May employ different LDC or VAR-Bias settings
    - <u>Strong</u> source (Reconfig) results in use of reverse regulation
      - May employ different Bandcenter, Bandwidth, and LDC or VAR-Bias settings



#### Simulation of LTC Transformer/Regulator with Two sources: Simplified Model



Initial condition: LTC neutral tap position, Source 1 and 2 voltages are each 100%, no reactive current flow (unity power factor)<sup>83</sup>



#### **Simulation Results**

Case #	DPI <sub>1</sub>	DPI <sub>2</sub>	Reactive Current (I <sub>x</sub> ) Through the transformer	VT <sub>1</sub>	VT <sub>2</sub>	ΔV
1	2%	$\infty$	0	100%	100.625%	.625
2	$\infty$	2%	0	99.375%	100%	.625
3	2%	20%	1.953 %	99.96 %	100.4%	.04
4	20%	2%	1.953 %	99.6 %	100.035%	.04
5	2%	2%	7.14 %	99.85%	100.14%	.29

- 1 & 2: System reconfiguration; one source, radial
- 3 & 4: DER (weak) vs. System (strong)
- 5: Two weak sources

Vormally expect 0.625V per tap



#### Autodetermination of Source Strength with RPF

- □ When RPF is detected, operation is set initially to "DG Mode"
- $\Box$   $\Delta V$  is measured for two tap operations:

 $\Delta V = V_{MBT} - V_{MAT}$ where  $V_{MBT}$  = measured load side voltage just before a tap change  $V_{MAT}$  = measured load side voltage one second after the tap change

- If the measured ∆V is > 0.47 (75%) of the normal expected value (0.625V) for two consecutive tap changes, Autodetermination will maintain "DG Mode" operation
- If the measured ∆V is <= 0.31V (50%) of the normal expected value (0.625V) for two consecutive tap changes, Autodetermination changes to "Regulate Reverse Mode" operation





# Reverse Power Source Strength Determination: User Manually Designates





## Reverse Power Source Strength Determination: Autodetermination





# REGC/LTCC: Autodetermination of Operating Mode with Reverse Power





# Handling DER rapid output change

- Irradiance and wind velocity can change very quickly
- Large rise or drop in power (W, VAR) can cause large voltage swings
- Normal fixed timing in OLTCs may not respond fast enough for good control
- Employ inverse response curve for time delay
  - Small changes yield longer time delays
  - Large changes yield shorter time delays



## Definite Time OLTC Characteristic





#### **Inverse Curve OLTC Time Characteristic**







Time = 120 sec. \* 0.34 = 40.8 = 41 sec.



# Protection Concepts and DER Issues

- Bidirectional Fault Current & Directionalization
  Reclosing treatment :
  - Increase of 1st Shot Time Delay
  - Adaptive protection with voltage control of reclosing
- Ferroresonance on islanded feeder s
- Ungrounded fault backfeed into transmission protection



## **IEEE Distribution Practices Survey – 1/02**

# **Impact on Utility Protection**

- No effect 22%
- Revised feeder coordination 39%
- Added directional ground relays 25%
- Added direction phase relays 22%
- Added supervisory control 22%
- Revised switching procedures 19%



## **Bidirectional Fault Currents: Coordination**

- Use directional elements in substation protection, mid-line reclosers and DER
  - Substation
    - Directionalize using 67 and 67N (instead of 50/51 and 50/51N)
    - Trip toward DER (downstream) to avoid sympathy trips for out-of-section faults
    - Trip toward Substation for remote breaker failure
  - Reclosers
    - Directionalize using 67 and 67N (instead of 50/51 and 50/51N)
    - Trip toward Substation for remote breaker failure
  - DER
    - Directionalize using 67 and 67N (instead of 50/51 and 50/51N)
    - Trip direction away from DER (upstream)















# IEEE Distribution Practices Survey – 1/02

# **DER Impact on Utility Reclosing**

- Revise reclosing practices 50%
- Added voltage relays to supervise reclosing 36%
- Extend 1<sup>st</sup> shot reclose time 26%
- Added transfer trip 20%
- Eliminate reclosing 14%
- Added sync check 6%
- Reduce reclose attempts 6%



## Utility Reclosing Issues: It is all about time.....



- If high-speed reclosing is employed, the DER interconnection protection must be faster!
- Clearing time includes protection operation and breaker opening



#### Utility Reclosing Issues: It is all about time







Use minimal dead time and voltage supervision for the reconnect t/reenergize permissive



## Voltage Supervised Dead Time





## Ferroresonance

- Ferroresonance can take place between an induction generator and capacitors after utility disconnection from feeder
  - Ferroresonance can also occur from Synchronous Generators and Inverter-based DER!
- Generator is excited by capacitors if the reactive components of the generator (X<sub>G</sub>) and aggregated capacitors (X<sub>C</sub>) are close in value
- This interplay produces non-sinusoidal waveforms with high voltage peaks. This causes transformers to saturate, causing non-linearities that exacerbate the problem.



Ferroresonance: Test Circuit Setup

New York Field Tests- 1989 Field Test Circuit (NYSEG)





## Ferroresonance: Observed Waveforms

New York Field Tests - 1989

Field Test Circuit (NYSEG)



**Conditions:** 

Wye-Wye Transformers, 100kVAr capacitance, 60kW generator, 12kW load







- May be applied at DER Interconnection (PoI)
- May be applied at feeder origin to detect ferroresonance after feeder is islanded (line side of CB)



## Sensing Ungrounded System Ground Faults

#### **DER Fault Backfeed**


### **MIPSYCON 2017**







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